

Application Note

# AYLA PIN MAPPING FOR INVENTEK ISM43362A WI-FI MODULE



Version: 1.1

Date Released: December 21, 2015

Document Number: AY006ASP0-1.1

 Ayla Networks

---

## Table of Contents

<b>Introduction .....</b>	<b>3</b>
<b>Audience .....</b>	<b>3</b>
<b>Related Documentation .....</b>	<b>3</b>
<b>Customer Support .....</b>	<b>3</b>
<b>Signal Pin-outs .....</b>	<b>4</b>
<b>Additional Pins .....</b>	<b>4</b>
<b>Programming Pins .....</b>	<b>5</b>
<b>Programming Header .....</b>	<b>6</b>

## LIST OF TABLES and FIGURES

TABLE 1 INVENTEK ISM43362A ADDITIONAL MODULE PINS .....	4
TABLE 2 INVENTEK ISM43362A PROGRAMMING MODULE PINS .....	5

---

## Introduction

This document describes the pin mapping for the Ayla features of the Inventek ISM43362A Module.

## Audience

This document is intended for programmers and hardware engineers that need to connect the Inventek ISM43362A module to other hardware components. This document contains descriptions of the Ayla signals present on each of the ISM43362A pins.

## Related Documentation

Ayla Module and MCU Interface Specification (AY006MSP0)  
Ayla Setup for Inventek ISM4362 Modules (AY006MIV0)

## Customer Support

Support and customer documentation is located at <http://support.aylanetworks.com>

## Signal Pin-outs

The pin mapping for the Ayla Enabled Module and the associated ISM43362A module pin names and pin numbers are shown below.

Ayla Signal Name	ISM43362A Pin Name	ISM43362A Module Pin	I/O	Description
SPI_SSN	ADC1/SPI_SSN	12	I/O	SPI Slave Select
SPI_SCK	ADC2/SPI_SCK	11	I/O	SPI Clock
SPI_MOSI	ADC4/SPI_MOSI	9	I/O	SPI Master Out Slave In
SPI_MISO	ADC3/SPI_MISO	10	I/O	SPI Master In Slave Out
UART_TX	TX	22	O	UART Transmit
UART_RX	RX	21	I	UART Receive
UART_CTSn	DM	19	I	UART CTS
UART_RTSn	DP	18	O	UART RTS
READY_N	RES	32	O	Module Initialized
INTR_N	RES	31	O	Interrupt output
RESET_N	RSTN	34	I	Module Reset
LINK_N	RES	30	O	ADS Link Status
WKUP	Wakeup	16	I	Wake Up Module
VDD	VDD	2, 14	PI	DC supply for Module
GND	GND	1,3,17,20,35, 36,37,38,39, 40,41,42,43,44	-	Ground

## Additional Pins

Some of the additional pins on the ISM43362A module that are required for normal operation are listed below.

ISM43362A Pin Name	ISM43362A Module Pin	I/O	Description
VBAT	15	PI	Power supply for backup circuitry

**Table 1 Inventek ISM43362A Additional Module Pins**

## Programming Pins

The pins on the ISM43362A module required to program and update the firmware are listed below in Table 2. These pins are optional and required only to provide the ability to perform firmware download to the module.

ISM43362A Pin Name	ISM43362A Module Pin
CFG1	29
CFG0	28
TDRSTN	8
TDO	7
TDI	6
TMS	4
TCK	5

**Table 2 Inventek ISM43362A Programming Module Pins**

## Programming Header

The pins required to flash and update the module along with the recommended header configuration is shown Figure 1.

RSTN	1	2	VDD
TMS	3	4	RXD
GND	5	6	TXD
TCK	7	8	GND

**Figure 1 Programming header**